

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shekhar Y. Borkar et al. Art Unit: 2816

Serial No.: 09/802,584 Examiner: Cassandra Cox
Filed: March 8, 2001 Assignee: Intel Corporation
Title: A SYNCHRONOUS CLOCK GENERATOR FOR INTEGRATED CIRCUITS

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT

In response to the Office action mailed October 3, 2003, please amend the application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 5 of this paper.

Remarks begin on page 11 of this paper.

DEC 22 2003 Technology center 2800

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

December 15, 200

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